

ABSTRACT OF THE DISCLOSURE

5 The electrical circuitry for a multiplier system includes a counter for determining proximity to sampling operation, and a switch to select between symmetrical noise invariant operation and a low-power mode of operation. A noise invariant circuit disables row skip operation in a multi-row multiplier, to enable analog sampling. Disabling of the row skip operation is accomplished at a time which is several 10 digital cycles preceding the time of analog sampling. Power saving multiplier row skippage resumes after analog sampling is completed.